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parallel Viterbi decoder; convolutional encoder

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Convolutional codes and their performance in communication systems- ► sjsu.edu [PDF]

A Viterbi - Communications, IEEE Transactions on [legacy, pre-1988], 1971 - ieeexplore.ieee.org
... **VITERBI : CONVOLUTIONAL CODES** ... message) sequences are equally likely, the **decoder**
which minimizes ... error probability for any code, block or **convolutional**, is one ...

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High-performance VLSI architecture for the Viterbi algorithm

M Boo, F Arguello, JD Bruguera, R Doallo, EL ... - IEEE transactions on communications, 1997 - ieeexplore.ieee.org

... diagram: in the case of a **convolutional** coder with a ... The canonic cascade architecture
of the **Viterbi decoder** (CCVD) [8], [9] is another **parallel** solution in ...

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Implementing the Viterbi algorithm

HL Lou - IEEE Signal processing magazine, 1995 - ieeexplore.ieee.org

... For the most commonly used **convolutional** codes, the inputs and ... a rate 1/2, 4-state
convolutional code ... sent into the **Viterbi decoder** the **Viterbi decoder**, one can ...

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Algorithms and architectures for concurrent Viterbi decoding

HD Lin, DG Messerschmitt - IEEE International Conference on Communications, 1989. ICC' ..., 1989 - ieeexplore.ieee.org

... a table lookup method or **parallel/pipelined** approaches ... 4, rate 1/2 optimal **convolutional**
code transmitted ... with a hard-quantized **Viterbi decoder** of truncation ...

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Generalized trace-back techniques for survivor memory management in the Viterbi algorithm

R Cypher, CB Shung - The Journal of VLSI Signal Processing, 1993 - Springer

... introduced a **decoding algorithm** for **convolutional** codes which has ... to be performed
in **parallel** the RAM ... for Survivor Memory Management in the **Viterbi Algorithm** ...

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A dynamically reconfigurable adaptive viterbi decoder- ► lu.edu [PDF]

S Swaminathan, R Tessier, D Goeckel, W Burleson - Proceedings of the 2002 ACM/SIGDA tenth international ..., 2002 - portal.acm.org

... Although [5] al- lowed for **parallel trellis** evaluation, the limited ... Initially, at
t=0, the **decoder** state is ... Like the **Viterbi** trellis, two branches emanate ...

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Viterbi decoding for satellite and space communication

J Heller, I Jacobs - IEEE Transactions on Communication Technology, 1971 - ieeexplore.ieee.org

... a particular implementation of a **Viterbi decoder** is discussed ... digital processing
by the **decoder**, the continuous ... 2. (a) $K = 3$, $RN = 1/2$ **convolutional** encoder. ...

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Systolic array processing of the Viterbi algorithm

CY Chang, K Yao - IEEE Transactions on Information Theory, 1989 - ieeexplore.ieee.org

... throughput rate as compared to the fully **parallel Viterbi decoder**. ... The **Viterbi decoding process** can be viewed in ... Let us consider a **convolutional** code of rate b ...

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Novel **Viterbi decoder** VLSI implementation and its performance

S Kubota, S Kato, T Ishitani - IEEE Transactions on Communications, 1993 - ieeexplore.ieee.org

... **VITERBI DECODERS REVIEWED** ... a **convolutional encoder** which generates n encoded bits is a function of the ... One is a path memory cell matrix (N , **parallel** and T -length ...

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A reconfigurable, power-efficient adaptive **Viterbi decoder**- umass.edu [PDF]

R Tessier, S Swaminathan, R Ramaswamy, D Goeckel, ... - IEEE Transactions on Very Large Scale Integration (VLSI) ... , 2005 - ieeexplore.ieee.org

... of se-quential control with **parallel decoding** may provide ... [2] F. Chan and D. Haccoun,

"Adaptive **Viterbi decoding** of **convolutional** codes over memoryless ...

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Key authors: **A Viterbi** - **F Adachi** - **M Sawahashi** - **H Suda** - **L Hanzo**

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